



Chirala Engineering College

CLASS TIME TABLE			
Department of Electrical & Electronics Engineering			
B. Tech –Electrical and Electronics Engineering (R13) w.e.f:29/06/2015			
Academic Year:2015-2016	Term: 29/06/2015 to21/11/2015	Batch: 2013-2017	
Year: III	Semester: I	Section-1	Class Room:127

DAY / HOURS	1	2	3	4	12.00 - 12.40	5	6	7
	8.30 AM 9.30 AM	9.30 AM 10.20 AM	10.20 AM 11.10 AM	11.10 AM 12.00 PM		12.40 PM 1.30 PM	1.30 PM 2.20 PM	2.20 PM 3.10 PM
MON	PS-II	EM-II / CS LAB			LUNCH BREAK	LDICA (TUT)		EM-III
TUE	LDICA	PE	MEFA (TUT)			PS-II	SPORTS/DEPT ACT	
WED	EM	PE	LDICA	MEFA		EM-III (TUT)		LIB/NET
THU	PE	EM-III	IPR	EM		PS-II (TUT)		COUNSL
FRI	MEFA	EM-II / CS LAB				IPR	EM (TUT)	
SAT	EM-III	MEFA	PS-II	EM		LDICA	PE (TUT)	

THEORY					
S.N	Sub Code	Subject	Credits	Name of the Faculty	Dept
1		Power Electronics	3	P.Bala Nagu	EEE
2		Electrical Machines-III	3	G.Joga Rao	EEE
3		Power System-II	3	A.Anand Kumar	EEE
4		Linear & Digital IC Applications	3	V.Srinivasa Rao	ECE
5		Electrical Measurements	3	A.Rakesh	EEE
6		Managerial Economics and Financial Analysis	3	D.Maheswari	MBA
7		Intellectual Property Rights and Patents (IPR)	2	D.Maheswari	MBA

PRACTICAL					
8		Electrical Machines Lab (EM-II LAB)	2	G.Joga Rao/ M.Anil Prakash	EEE
9		Control System Lab(CS LAB)	2	V.Siva nagaraju/J.Anusha	EEE

ACTIVITIES					
1	LIB	Central Library			
2	TUT	Tutorial			
3	SLIP	Slip Test			
4	SPORTS	Sports			
5	DEPT.ACT	Departmental Activities			
6	COUNSL	Counseling			

CLASS IN-CHARGE	STUDENT REPRESENTATIVES	ROLL NO.
G.Joga Rao		

HOD

PRINCIPAL