



Chirala Engineering College

CLASS TIME TABLE			
Department of Electronics & Instrumentation Engineering			
B. Tech – Electronics & Instrumentation Engineering (R13) w.e.f:29-06-2015			
Academic Year:2015-2016	Term: 29-06-2015 to 21-11-2015	Batch: 2013-2017	
Year: III	Semester: I	Section:1	Class Room:

DAY / HOURS	1	2	3	4	12.00	5	6	7
	8.30 AM 9.30 AM	9.30 AM 10.20 AM	10.20 AM 11.10 AM	11.10 AM 12.00 AM	- 12.40	12.40 PM 1.30 PM	1.30 PM 2.20 PM	2.20 PM 3.10 PM
MON	DSD&DICA	PDC	LICA	CS	Lunch Break	PDC LAB		
TUE	DSD&DICA	LICA				PDC	CS	COUNSL
WED	PDC	CS	DSD&DICA	LICA		LICA LAB		
THU	DSD&DICA	CS	DSP	DSP		PDC	LICA	LIB
FRI	LICA	DSD & DICA LAB				CS	PDC	NET
SAT	CS	LICA	DSP	DSP		PDC	DSD&DICA	SPORTS

THEORY					
S.N	Sub Code	Subject	Credits	Name of the Faculty	Dept
1		Linear IC Applications	4	K.Padma	ECE
2		Digital Signal Processing	4	N.Suresh Babu	ECE
3		Control Systems	4	M.Sumalatha	ECE
4		Digital System Design & DICA	4	K.Harikrishna	ECE
5		Pulse and Digital Circuit	4	H.Raghunatha Rao	ECE
PRACTICAL					
6		PDC LAB	2	H.Rghunatha Rao	ECE
7		LICA LAB	2	K.Padma	ECE
8		DSD & DICA LAB	2	K.Harikrishna	ECE
ACTIVITIES					
1	LIB	Central Library			
2	TUT	Tutorial			
3	SLIP	Slip Test			
4	SPORTS	Sports			
5	DEPT.ACT	Departmental Activities			
6	COUNSL	Counseling			
CLASS IN-CHARGE		STUDENT REPRESENTATIVES		ROLL NO.	
N.Mallikarjun Rao					

HOD

PRINCIPAL