



# Chirala Engineering College

## CLASS TIME TABLE

### Department of Electrical & Electronics Engineering

B. Tech –Electrical and Electronics Engineering (R10) w.e.f:14-07-2015

Academic Year:2015-2016

Term:14/07/2015 – 05/12/2015

Batch: 2012-2016

Year: IV

Semester: I

Section-1

Class Room:128

DAY / HOURS	1	2	3	4	12.00 - 12.40	5	6	7
	8.30 AM 9.30 AM	9.30 AM 10.20 AM	10.20 AM 11.10 AM	11.10 AM 12.00 PM		12.40 PM 1.30 PM	1.30 PM 2.20 PM	2.20 PM 3.10 PM
MON	CO	EACM	EDS (TUT)		Lunch Break	SGP	CO	PSOC
TUE	SGP	PSOC	EACM (TUT)			HVE	SPORTS/DEPT ACT	
WED	HVE	EACM	CO	EDS		SGP	PSOC (TUT)	
THU	PSOC	MPMC/ES LAB				HVE (TUT)	COUNSL	
FRI	EACM	CO	SGP (TUT)			MPMC/ES LAB		
SAT	EDS	PSOC	SGP	HVE		CO (TUT)	LIB/NET	

### THEORY

S.N	Sub Code	Subject	Credits	Name of the Faculty	Dept
1	R4102B	Energy Audit, Conservation &Mgmt(OE)	3	Dr. K.Sudhakar Reddy	EEE
2	R41023	Switch Gear and Protection	3	P.Suresh	EEE
3	R41022	High Voltage Engineering	3	G.Joga Rao	EEE
4	R41024	Power System Oper. and Control	3	J.Anusha	EEE
5	R4102A	Electrical distribution System(E-1)	3	P.BalaNagu	EEE
6	R41021	Computer Organization	3	D.Dwija	EEE

### PRACTICAL

1		Electrical Simulation Lab	2	D.Dwija/B.Rajanna	EEE
2		Microprocessor & Microcontroller	2	Veeraiah/ K.Samba Siva Rao	ECE

### ACTIVITIES

1	LIB	Central Library			
2	TUT	Tutorial			
3	SLIP	Slip Test			
4	SPORTS	Sports			
5	DEPT.ACT	Departmental Activities			
6	COUNSL	Counseling			

CLASS IN-CHARGE	STUDENT REPRESENTATIVES	ROLL NO.
D.Dwija	I.K.Chandu Maharaj	12E91A0204

HOD

PRINCIPAL