



Chirala Engineering College

CLASS TIME TABLE			
Department of Electronics & Communication Engineering			
B. Tech – Electronics & Communication Engineering (R13) w.e.f:19/01/2015			
Academic Year:2014-2015	Term: 19/01/2015 to 30/05/2015	Batch: 2013-2014	
Year: II	Semester: II	Sec:1	Class Room:312

DAY / HOURS	1	2	3	4	11.50 AM - 12.40 PM	5	6	7
	8.30 AM 9.20 AM	9.20 AM 10.10 AM	10.10 AM 11.00 AM	11.00 AM 11.50 AM		12.40 PM 1.30 PM	1.30 PM 2.20 PM	2.20 PM 3.10 PM
MON	AC	STLD	ENG.PROF	ENG.PROF	Lunch Break	←-----ECA/AC LAB-----→		
TUE	AC	EMWTL	RV&SP	LIB/NET		ECA	MS	STLD(TUT)
WED	EMWTL	AC	ECA	RV&SP		←-----ECA/AC LAB-----→		
THU	RV&SP	MS	AC(TUT)	EMWTL		ECA(TUT)	STLD	MS(TUT)
FRI	RV&SP	EMWTL	ECA	STLD		COUNSELLING	SPORTS/DEPT.ACT	
SAT	EMWTL	MS	STLD	ECA		EMWTL(TUT)	AC	RV&SP(TUT)

THEORY					
S.N	Sub Code	Subject	Credits	Name of the Faculty	Dept
1	NA	Switching Theory & Logic Design(STLD)	3	Mr. S. Suman	ECE
2	NA	Analog Communications(AC)	3	Miss. K. Mercy	ECE
3	NA	Random Variables & Stochastic Processes(RV&SP)	3	Mrs. M. Sumalatha	ECE
4	NA	Electro Magnetic Waves & Transmission Lines(EMWTL)	3	Mr. K. Hari Krishna	ECE
5	NA	Electronic Circuit Analysis(ECA)	3	Mr. N.Suresh Babu	ECE
6	NA	Management Science(MS)	3	Mr. Y. Tiru Sameer	ECE
7					
PRACTICAL					
1	NA	ECA LAB	2	Mrs. Ramadevi/Mrs. Sumalatha	ECE
2	NA	AC LAB	2	Mr. Samba/Miss. Mercy	ECE
3					
ACTIVITIES					
1	LIB	Central Library			
2	SEM	Seminar			
3	TUT	Tutorial			
4	SLIP	Slip Test			
5	SPORTS	Sports			
6	DEPT.ACT	Departmental Activities			
7	COUN	Counseling			
CLASS IN-CHARGE		STUDENT REPRESENTATIVES		ROLL NO.	
Mrs. M.Sumalatha		1. CH.Manikanth 2. N.Lakshmi Subhashini		13E91A0443 13E91A0434	

HOD

PRINCIPAL



Chirala Engineering College

CEC